

HP-41C Exchange Quads by Bob Edelen & Lee Vogel  
PPC Calculator Journal V8 N6 Pg 49 Aug-Dec 1981

```
01 LBL "QUAD1" ;Exchange Quad Memory or Port Extender
02 LBL "Q"
03 STOP
04 RTN
05 LBL "Sc" ;Store C register
06 "\1F\F0\01\69\1F\F0\06"
07 RCL M
08 X<> c
09 STO 00
10 "Q"
11 ASTO L
12 "\C0\00\09\77\86\8E\CE\00\91\7D"
13 ">\77\86\AE\74\C0\00\29"
14 ">\01SW-R/S"
15 E8
16 STO b
17 END
```

```
004BC000F6005155414431C000F200518485C000F3005363F7
1FF001691FF0069075CE7D30F1519A74FAC0000977868ECE00
917DF87F7786AE74C00029F87F0153572D522F531B18917CC0
000D00
```

75 BYTES

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Program Registers Needed: 11

Row 1 (1 - 2)



Row 2 (2 - 6)



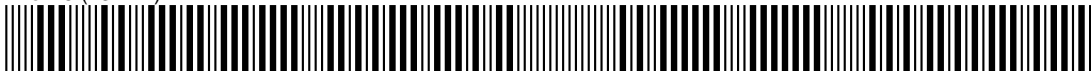
Row 3 (6 - 11)



Row 4 (12 - 13)



Row 5 (13 - 14)



Row 6 (14 - 17)

